



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hisashi Shimizu et al.

Art Unit : 2814

Serial No.: 09/536898

Examiner: Louie, Wai Sing

Filed

: March 28, 2000

Title

: HYBRID INTEGRATED CIRCUIT DEVICE

Commissioner for Patents Washington, D.C. 20231

INFORMATION RELATING TO COPENDING US PATENT APPLICATION

In order to fulfill the duty to disclose related or material co-pending applications under 37 CFR 1.56, the Examiner's attention is brought to Co-pending US Application Serial No. 09/536,921, which was filed on March 28, 2000 and was allowed on November 4, 2002. The copending application may be related to the present application.

Please apply any charges if necessary or credits to Deposit Account No. 06-1050.

Respectfully submitted,

1/29/03

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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